```
YYY
YYY
YYY
YYY
YYY
                      777
                                                   $$$$$$$$$$
$$$$$$$$$$
$$$$$$$$$$
```

Ps

YZ

ZS

ZS

78

78

ZS

28

ZS

ZS

ZS

ZS

ZS

ZS

RRRRRRRR RR	RRRRRRRR RRRRRRRR RR RR RR RR RR RR RRRRRR	000000 00 00 00 00	RRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRR	000000 00 00 00 00	GGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGGG	::
	\$					

ERRORLOG Table of contents	- ERROR LOG SUPPORT ROUTINES	Н	4	16-SEP-1984 00:04:39	VAX/VMS Macro V04-00) Page	0
(1) 198 (1) 315 (1) 407 (1) 492 (1) 583 (1) 644 (1) 703 (1) 739 (1) 803 (1) 844 (1) 876	UNEXPECTED INTERRUPT SERVICE LOG DEVICE ERRORS LOG ASYCHRONOUS DEVICE ATTENTIONS LOG SOFTWARE STATUS LOG DRIVER MESSAGE ERL\$LOG DMSCP and ERL\$LOG TMSCP BUILD STARTUP AND POWERFATL MESSAGES ALLOCATE ERROR MESSAGE BUFFER GET FULL DEVICE NAME RELEASE ERROR MESSAGE BUFFER WAKE ERROR LOG FORMAT PROCESS						

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ERRORLOG - ERROR LOG SUPPORT ROUTINES .TITLE

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D. N. CUTLER 7-MAR-77

ERROR LOG SUPPORT ROUTINES

MODIFIED BY:

EADO162 Elliott A. Drayton 26-Apr-Correct ADDB3 in routine GETFULLNAME to use RO. V03-012 EAD0162 26-Apr-1984

V03-011 EAD0160 Elliott A. Drayton 16-Apr-1984 Added a test for the system block address not being there.

EADO137 Elliott A. Drayton 11-Apr-1984 Changed code to log full device names. NODE NAME + DEVICE. V03-010 EAD0137

LMP0221 L. Mark Pilant, 30-Mar-1984 1 Change UCB\$L_OWNUIC to ORB\$L_OWNER and UCB\$W_VPROT to ORB\$W_PROT. 30-Mar-1984 13:57 V03-009 LMP0221

KPL0100 Peter Lieberwirth 22-Mar-1984 Use CONFREGL instead of CONFREG. Anticipate SBICONF containing a PFN instead of a VA if BI adapter initialization didn't originally recognize the adapter. V03-008 KPL0100

V03-007 SSA0007 SSA0007 Stan Amway Fix broken branch to ERL\$ALLOCEMB. 2-feb-1984

V03-006 LMP0185 L. Mark Pilant, 1-Feb-1984 9:37 fix some broken branches.

V03-005 ROW0241 12-0CT-1983 Ralph O. Weber

4901234567

2222222222235555555555556444444444

ERRORLOG V04-000	- ERROR LOG	SUPPORT	ROUTINES	J 4 16-SEP-1984 00:04:39 VAX/VMS Macro V04-00 Page 2 5-SEP-1984 03:41:34 [SYS.SRC]ERRORLOG.MAR;1 (1
	0000 0000 0000 0000 0000 0000 0000 0000 0000	58 : 59 : 60 :		Correct ERL\$LOG_D(T)MSCP to allocate space for the error log header in addition to the space needed for the logged message etc. Also use symbolic size of error log entry header instead of a constant.
	0000 0000 0000	60 61 623 645 667 667 667 71	v03-004	RLRMSCP Robert L. Rappaport 27-Jul-1983 Add two entrypoints, ERL\$LOG_DMSCP and ERL\$LOG_TMSCP, to log invalid Disk MSCP and Tape MSCP messages.
	0000	67 :	v03-003	KDM0051 Kathleen D. Morse 11-Jul-1983 Change references to TODR to use loadable, cpu-dependent
	0000 0000 0000 0000	70 71 72 73 -	v03-002	KDM0051 Kathleen D. Morse 11-Jul-1983 Change references to TODR to use loadable, cpu-dependent routine, EXE\$READ_TODR. BLS0187 Benn Schreiber 24-Sep-1982 Correct broken branch offset due to UCB growing

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```
16-SEP-1984 00:04:39 VAX/VMS Macro V04-00 
5-SEP-1984 03:41:34 [SYS.SRCJERRORLOG.MAR;1
```

```
MACRO LIBRARY CALLS
                                                                      DEFINE CDDB OFFSETS
DEFINE CDRP OFFSETS
DEFINE DDB OFFSETS
DEFINE DDT OFFSETS
DEFINE DEVICE CHARACTERISTIC BITS
ERROR LOG MESSAGE BUFFERS OFFSETS
DEFINE ERROR ALLOCATION BUFFER OFFSETS
DEFINE FCB OFFSETS
DEFINE I/O FUNCTION VALUES
                   $CDDBDEF
                   SCDRPDEF
                   $DDBDEF
                   SDDTDEF
                   SDEVDEF
                   SEMBDEF <DV,SU,TS,UI,SP,LM,ET>
                   SERLDEF
                   SFCBDEF
                   SIODEF
                                                                      DEFINE I/O FUNCTION VALUES
DEFINE IRP OFFSETS
DEFINE MACHINE CHECK RECOVERY MASK BITS
DEFINE MSCP OFFSETS
DEFINE NEXUS DEVICE TYPE CODES
DEFINE OBJECT'S RIGHTS BLOCK OFFSETS
DEFINE PROCESSOR REGISTER NUMBERS
DEFINE SYSTEM BLOCK OFFSETS
DEFINE UCB OFFSETS
DEFINE WCB OFFSETS
                   SIRPDEF
                   SMCHKDEF
                   SMSCPDEF
                   SNDTDEF
                   SORBDEF
                   SPRDEF
                   $SBDEF
                   SUCBDEF
                   SWCBDEF
                                                                      *** INTERRUPT IDENTIFIES VECTOR #
      :DEBUG=1
102
103
104
105
     LOCAL MACROS
106
107
         MACRO TO DEFINE AN INTERRUPT SERVICE ROUTINE LABEL FOR UNEXPECTED INTERRUPTS
108
                   .MACRO ISRDEF, VNUM
                                                                      : Make all vectors long word alligned :INTERRUPT SERVICE LABEL
                   .ALIGN LONG
      ERL$VEC'VNUM::
                               DF DEBUG
ERLSUNEXP
                                                                       : *** IF DEBUGGING
                                                                       ***CALL INTERRUPT SERVICE
                   BSBW
                   .BYTE
                               <VNUM>/2
                                                                       :***IDENTIFY VECTOR OFFSET INTO SCB
                   .ENDC
                   .ENDM
                               ISRDEF
         MACRO TO DEFINE THE INTERRUPT SERVICE ROUTINE LABELS FOR AN ADAPTER
      VECTOR = SLOT * 4 + 256
                    REPT
                   ISRDEF \VECTOR
      VECTOR = VECTOR + <16 * 4>
                   .ENDR
                               NDF DEBUG
ADP HANDLER
                                                                      : IF NOT DEBUGGING
: CALL INTERRUPT SERVICE
                   BSBB
                   .ENDC
                               ADPISR
         LOCAL SYMBOLS
```

```
MAXIMUM NUMBER OF MESSAGES BEFORE WAKE OF FORMAT PROCESS
A000000A
                            MAXMSG=10
                              MAXIMUM TIME IN SECONDS BEFORE WAKE OF FORMAT PROCESS
0000001E
                            MAXTIM=30
                       1447
1449
1153
1155
1155
1159
                            ; LOCAL DATA
        0000000
                                       .PSECT $$$260,QUAD,WRT
                                      WARNING!!! The next two bytes must be adjacent and word aligned
                                       .ALIGN
                                                 WORD
       00
                            BUF1:
                                       BYTE.
                                                                                  COUNT OF BUSY MESSAGES IN BUFFER COUNT OF COMPLETED MESSAGES IN BUFFER
             0001
                                       BYTE
                                                                                  BUFFER INDICATOR
                                                                                 BUFFER CONTROL FLAGS
ADDRESS OF NEXT AVAILABLE SPACE IN BUFFER
ADDRESS OF END OF BUFFER + 1
                                       BYTE
        ÕÕ
00000000
                                       . LONG
                       160
161
162
163
164
165
000002001
                                                 20$
512-ERL$C_LENGTH
                                       . LONG
00000200
                                       .BLKB
                                                                                  ; ACTUAL BUFFER AREA
                            20$:
                                                                                  : REF LABEL
                                      WARNING!!! The next two bytes must be adjacent and word aligned
                       166
167
168
                                       .ALIGN
                                                 WORD
                            BUF2:
                                                                                  COUNT OF BUSY MESSAGES IN BUFFER COUNT OF COMPLETED MESSAGES IN BUFFER
                                       BYTE
        ÕÕ
                       169
170
171
                                                                                 BUFFER INDICATOR
BUFFER CONTROL FLAGS
ADDRESS OF NEXT AVAILABLE SPACE IN BUFFER
ADDRESS OF END OF BUFFER + 1
        Ŏ1
                                       BYTE
                                       .BYTE
                                       .LONG
00000400
                                                 20$
512-ERL$C_LENGTH
                                       . LONG
00000400
                                                                                  ACTUAL BUFFER AREA
                                       .BLKB
                            20$:
                                                                                  : REF LABEL
                              GLOBAL DATA
                              ERROR LOG DATA BASE
                           ERL$AL_BUFADDR::
                                                                                 :ALLOCATION BUFFER ADDRESS ARRAY :ADDRESS OF BUFFER 1 DESCRIPTOR :ADDRESS OF BUFFER 2 DESCRIPTOR
                                       .LONG
000000000
                                                 BUF 1
                                       LONG
                                                 BUF 2
                            ERL$GB_BUF IND::
                                                                                  CURRENT ALLOCATION BUFFER INDICATOR
                                       .BYTE
                            ERL$GB_BUFFLAG::
                                                                                  BUFFER STATUS FLAGS
        00
                                       .BYTE
```

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- ERROR LOG SUPPORT ROUTINES

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00 040A 190 BYTE 0
00 040B 191 ERL\$GB_BUFTIM::
1E 040B 192 BYTE MAXTIM
00000000 040C 193 ERL\$GL_ERLPID::
00000000 040C 194 LONG 0
00000000 0410 195 ERL\$GL_SEQUENCE::
00000000 0410 196 LONG 0

16-SEP-1984 00:04:39 VAX/VMS Macro V04-00
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FORMAT PROCESS BUFFER INDICATOR
PROCESS ID OF ERROR LOG PROCESS
UNIVERSAL ERROR SEQUENCE NUMBER

M 4

ERRORLOG V04-000

```
ER
```

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ERRORLOG

00000002'8F

00000000 FF43

00000500

04

MOVL

CMPB BNEQ

MOVL

(R4),R1

R1,#NDTS_DR32

#*X500,(R4)

V04-000

VAX/VMS Macro V04-00 [SYS.SRCJERRORLOG.MAR;1

```
(1)
                                                    .SBTTL UNEXPECTED INTERRUPT SERVICE
                               ERL$VEC'VNUM - INTERRUPT SERVICE FOR SCB VECTOR VNUM.
ERL$UNEXP - GENERAL UNEXPECTED INTERRUPT SERVICE
                                         THESE INTERRUPT SERVICE ROUTINES ARE EXECUTED FOR UNUSED SCB VECTORS.
                                         IF DEBUG IS DEFINED, EACH INTERRUPT SERVICE CALLS ERLSUNEXP WITH THE <VECTOR OFFSET>/2 INTO THE SCB AS A 1 BYTE ARGUMENT.
                                         IF DEBUG IS NOT DEFINED, ALL CPU INTERRUPT SERVICE ROUTINES COLLAPSE TO GLOBAL LABELS EQUAL TO ERLSUNEXP AND ALL ADAPTER INTERRUPT SERVICE ROUTINES CALL A ROUTINE THAT SAVES THE ADAPTER TYPE, TRIES TO DISABLE FURTHER INTERRUPTS, AND LOGS THE INTERRUPT.
                                         THERE ARE ENOUGH INTERRUPT SERVICE ROUTINES FOR THE ARCHITECTURAL PAGE
                                         OF THE SCB, I.E., 128 ROUTINES.
                                         INPUTS:
                                                    (SP) = PC AT INTERRUPT
4(SP) = PSL AT INTERRUPT
                                         OUTPUTS:
                                                    ERROR IS LOGGED, OR PROCESSOR BUGCHECKS.
          00000000
                                                    .PSECT $AEXENONPAGED,LONG
                                        UNEXPECTED ADAPTER INTERRUPT HANDLER: IF DEBUG IS DISABLED, SAVE THE ADAPTER TYPE, ATTEMPT TO DISABLE FURTHER INTERRUPTS FROM THE ADAPTER, AND LOG THE INTERRUPT. IF DEBUG IS ENABLED, BUGCHECK AS FOR CPU INTERRUPTS.
                                                     ALIGN LONG
                                     ADP_UNEXP:
                                                    NEXUS = 0
                                                                                                             :FIRST ADAPTER = 0
:ISR'S FOR 16 ADAPTERS ONLY
:DEFINE ERLSINT'VNUM LABELS AND ISRS
00000000
                                                    ADPISR
                                                                  \NEXUS
                                                    NEXUS = NEXUS + 1
                                                                                                              :NEXT ADAPTER
                                                     ENDR
                                     ADP_HANDLER:
                                                                  #ADP_UNEXP+2,(SP)
#4,(SP)
#^M<RO,R1,R2,R3,R4>
5*4(SP),R3
                                                                                                               COMPUTE ADAPTER OFFSET NUMBER
          C2
C6
BB
D0
D0
18
                                                    SUBL
                                                    DIVL
                                                                                                             ; SAVE REGISTERS
; RETRIEVE SLOT NUMBER
; GET ADDRESS OF ADAPTER REGISTERS
; GEQ MEANS SBICONF DOES NOT CONTAIN
; A SYSTEM VA, MUST BE PFN OR 0
                                                    PUSHR
                                                    MOVL
                                                    MOVL
                                                                   ammg$GL_SBICONF[R3],R4
                                                    BGEQ
                                                    SPRTCTINI B^5$, #<MCHK$M_NEXM!MCHK$M_LOG>
                                                                                                             DISABLE ADAPTER INTERRUPTS (HOPEFULLY)
GET ADAPTER CONFIGURATION REG CONTENTS
IS THIS A DR32?
BRANCH IF NOT
ELSE CLEAR INTERRUPTS IN SPECIAL WAY
                                                                   4(R4)
```

54 64 D0 0076 255 1\$: MOVL (R4),R4 ;GET THE ADAPTER'S CONFIGURATION RE 37 50 E9 0079 257 00000000°FF43 D5 007D 259 00000000°FF43 D5 007D 260 0000000°FF43 54 9A 0086 262 00000000°FF43 54 9A 0086 263 00000000°FF43 54 9A 0086 264 00000251°EF 16 0091 265 04 A2 0061 8F B0 009A 267 14 50 E9 0097 266 04 A2 0061 8F B0 009A 267 10 A2 53 D0 00A0 268 11 A2 54 D0 00A4 269 00000325°EF 16 00A8 270 00000000000000000000000000000000000	
37 50 E9 007A 258 BLBC R0,100\$;IF RO LBC, THEN NO ADPATER PRESENT 00000000'FF43 D5 007D 260 TSTL @EXE\$GL_CONFREGL[R3] ;ALREADY CONFIGURED? 00000000'FF43 54 9A 0086 262 MOVZBL R4,@EXE\$GL_CONFREGL[R3] ;SAVE THE ADAPTER TYPE 00000000551'FF 16 00091 265 MOVL #EMB\$C_UI_LENGTH,R1 ;SET_SIZE OF MESSAGE TO ALLOCATE AN EPROPLICE BUSINESS.	
00000000'FF43 D5 007D 260 TSTL @EXE\$GL_CONFREGL[R3] ;ALREADY CONFIGURED? 00000000'FF43 54 9A 0086 262 MOVZBL R4,@EXE\$GL_CONFREGL[R3] ;SAVE THE ADAPTER TYPE 008E 263 10\$: 000000251'FF 16 0091 265 MOVL #EMB\$C_UI_LENGTH,R1 ;SET_SIZE OF MESSAGE TO ALLOCATE	
51 18 DO 008E 264 MOVL #EMB\$C_UI_LENGTH,R1 ;SET_SIZE OF MESSAGE TO ALLOCATE	
51 18 DO 008E 264 MOVL #EMB\$C_UI_LENGTH,R1 ;SET_SIZE OF MESSAGE TO ALLOCATE 00000251'EF 16 0091 265 JSB ERL\$ALCOCEMB ;ALLOCATE AN ERROR LOG BUFFER 14 50 E9 0097 266 BLBC R0,20\$;BRANCH IF NONE AVAILABLE 04 A2 0061 8F B0 009A 267 MOVW #EMB\$C_UI_EMB\$W_UI_ENTRY(R2) ;SET_MESSAGE TYPE 10 A2 53 DO 00A0 268 MOVL R3,EMB\$L_UI_TR(R2) ;SET_SLOT/TR NUMBER 14 A2 54 DO 00A4 269 MOVL R4,EMB\$L_UI_CSR(R2) ;SET_CONFIGURATION REGISTER VALUE 00000325'EF 16 00A8 270 JSB ERL\$RELEASEMB ;RELEASE BUFFER 1F BA 00AE 271 20\$: POPR #^M <r0,r1,r2,r3,r4> ;RESTORE REGISTERS 5E 04 CO 00B0 272 ADDL #4,SP ;REMOVE SLOT NUMBER</r0,r1,r2,r3,r4>	
1F BA 00AE 271 20\$: POPR #AM <ro,r1,r2,r3,r4> ;RESTORE REGISTERS 5E 04 CO 00BO 272 ADDL #4,SP ;REMOVE SLOT NUMBER 02 00B3 273 REI 00B4 274</ro,r1,r2,r3,r4>	
54 D4 00B4 275 100\$: CLRL R4 ;FLAG NO ADAPTER PRESETN D6 11 00B6 276 BRB 10\$;JOIN COMMON CODE 00B8 277	
00B8 278; 00B8 279; UNEXPECTED CPU INTERRUPT HANDLER: IF DEBUG IS ENABLED, BUGCHECK WITH 00B8 280; <vector offset="">/2 INTO SCB AS TOP BYTE ON STACK. IF DEBUG IS DISABLED, 00B8 281; JUST BUGCHECK. 00B8 282; 00B8 283 ALIGN LONG</vector>	
0000000 0088 284 CPU_UNEXP: 00000000 0088 285	
0088 290 0088 292 0088 293 0088 294 0088 295 0088 296 0088 296 0088 297 0088 297 0088 296 0080 297 0080 297 0080 297 0080 297 0080 297 0080 297 0080 298 0080 297 0080 299 02 0080 300 0080 300 0080 301 0080 302 0080 303 0080 303 0080 305 0080 306 0080 307 0080 307 0080 3080 3080 ReI 0080 Re	SET
00B8 296 BUG_CHECK UNXINTEXC ;BUGCHECK 00BC 297 IFT 00BC 298 TSTL (SP)+ ;***CLEAN STACK 00BC 299 .ENDC 02 00BC 300 REI ;RETURN FROM INTERRUPT 00BD 301;	
00BD 302; 00BD 303; Vector entry for counting unexpected interrupts, rather than logging 00BD 304; them. Used on 11/780 for passive release on the DW780 and for the 00BD 305; CVTP microcode bug. 00BD 306; 00BD 307 .ALIGN LONG	
00BD 307 .ALIGN LONG 00CO 308	
0000 308 0000 309 ERL\$VEC_RETURN:: 00000000°EF D6 0000 310 INCL IO\$GL_SCB_INTO : Increment counter 02 0006 311 REI : And return	

ERRORLOG V04-000 - ERROR LOG SUPPORT ROUTINES UNEXPECTED INTERRUPT SERVICE

312 313

00C7

C 5

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ERF VO4

OC A1

30 A1

DO

DO

: INSERT REQUESTER PROCESS ID

EMB\$L_DV_RQPID+4
EMB\$W_DV_BOFF+2
;INSERT TRANSFER PARAMETERS

ERI

```
.SBTTL LOG DEVICE ERRORS
                                                                                                                                      ERL$DEVICERR - LOG DEVICE CONTROLLER AND/OR DRIVE ERROR ERL$DEVICTMO - LOG DEVICE TIMEOUT ERROR
                                                                                                                                      THIS ROUTINE IS CALLED TO LOG A DEVICE TIMEOUT OR DEVICE CONTROLLER AND/OR DRIVE ERROR.
                                                                                                                                      INPUTS:
                                                                                                                                                             R5 = DEVICE UNIT UCB ADDRESS.
                                                                                                                                      OUTPUTS:
                                                                                                                                                            IF AN ERROR LOG ENTRY IS NOT ALREADY IN PROGRESS ON THE UNIT, ERROR LOGGING IS ENABLED FOR THE UNIT, AND THE CURRENT REQUEST DOES NOT INHIBIT ERROR LOGGING, THEN AN ERROR MESSAGE BUFFER IS ALLOCATED AND FILLED IN WITH PERTINENT REQUEST INFORMATION FOLLOWED BY A DUMP OF
                                                                                                                 THE DEVICE REGISTERS.
                                                                                                                                                             ALL REGISTERS ARE PRESERVED ACROSS CALL.
                                                                      00000000
                                                                                                                                                             .PSECT
                                                                                                                                                                                          WIONONPAGED
                                                                                     0000
                                                                                                                                                                ENABL
                                                                                                                                                                                          LSB
                                                                                                                 340
341
                                                                                                                             ERL$DEVICERR::
                                                                                                                                                                                                                                                                                      :LOG DEVICE CONTROLLER AND/OR DRIVE ERROR
                                                                     DD
11
                                                                                                                                                                                           #EMB$C_DE
                                                                                                                                                                                                                                                                                       SET FOR DEVICE ERROR
                                                                                                                                                             PUSHL
                                                                                                                                                                                   #EMB$C_DT,-(SP)
#EMB$C_DT,-(SP)
#DEV$V_ELG,UCB$L_DEVCHAR(R5),15$; IF SET, ERROR LOG ENABLED
#O$V_INHERLOG,UCB$W_FUNC(R5),12$; IF SET, ERROR LOG INHIBITED
UCB$W_ERRCNT(R5)
#UCB$V_ERLOGIP,UCB$W_STS(R5),40$; IF SET, ERROR IN PROGRESS
#O*KARO,R1,R2,R3,R6>
#O*KARO,R1,R2,R2,R2,R2
#O*KARO,R1,R2,R2,R2
#O*KARO,R1,R2,R2
#O*KARO,R1,R2
#O*KARO,R1,R2
#O*KARO,R1
#EMB$C_DT,-(SP)
#EMB$C_DT
                                                   ŎS
                                                                                                                342
344
344
346
347
349
                                                                                                                                                             BRB
                                                                                     0002
                                                                                     0004
                                                                                                                             ERL$DEVICTMO::
                                                                                     0004
                                                                     30
30
30
80
80
80
80
                               0060 8F
                                                                                                                                                             MOVZWL
        03 38 A5
                                                                                     0009
                                                                                                                                                             BBS
                                                                                     OOOE
                                           0081
                                                                                                                                                             BRW
F7 009A C5
0082
                                                                                     0011
                                                                                                                                                             BBS
                              0082 C5
A5 02
004F 8F
                                                                                     0017
                                                                                                                                                             INCW
                                                                                     001B
         72 64 A5
                                                                                                                                                             BBS
                                                                                                                 350
                                                                                                                                                             PUSHR
                                                  A5
C5
                     53
                                                                                                                                                             MOVL
                                                                     DO 30 16 E9 DO
                            0088
                                                                                                                                                             MOVL
              56
                                                                                                                                                             MOVZWL
                                        16
                                                  EF 50 52 04
                 00000251
                                                                                                                                                             JSB
                                        54
                                                                                                                                                             BLBC
              0094 C5
                                                                                                                                                             MOVL
        04 A2
                                                                     A8
B0
C0
                              A5
                                                                                                                                                             BISW
                                                  AE
1C
                                                                                                                                                             MOVW
                                                                                                                                                             ADDL
                                                                                                                 EMB$B_DV_TYPE EQ EMB$B_DV_CLASS+1
UCB$B_DEVCLASS(R5),(R2)+ ;INSERT_DEVICE CLASS AND TYPE
UCB$L_IRP(R5),R1 ;GET ADDRESS OF I/O PACKET
                                                                                                                                                             ASSUME
                                        40 A5
58 A5
                                                                     BO
                                                                                                                                                             MOVW
                                                                                                                                                             MOVL
                                                                                                                                                                                          EMB$L_DV_RQPID_EQ
IRP$L_PID(R1),(R2)+
                                                                                                                                                             ASSUME
                                                                                                                                                                                                                                                                                     EMB$B_DV_TYPE+1
```

EMB\$W_DV_BOFF EQ EMB\$W_DV_BCNT EQ IRP\$W_BOFF(R1),(R2)+

MOVL

MOVL

ASSUME ASSUME D 5

			- ER	ROR LOG	S SUPPORT ERRORS	ROUTINES	E 5 16-SEP-1984 00:04:39 VAX/VMS Macro V04-00 Page 5-SEP-1984 03:41:34 [SYS.SRC]ERRORLOG.MAR;1	10 (1)
82	00BC	C5	DO	005B 005B 0060	372 373	ASSUME MOVL	EMB\$L_DV_MEDIA EQ EMB\$W_DV_BCNT+2 UCB\$L_MEDIA(R5),(R2)+ ;INSERT SIZE OF DISK	
82	54	A5	в0	0060 0060 0064 0064	375 376 377	ASSUME	EMB\$W_DV_UNIT EQ EMB\$L_DV_MEDIA+4 UCB\$W_UNIT(R5),(R2)+ ;INSERT UNIT NUMBER	
82	0082	C5	В0	0064 0064 0069	378 379	ASSUME MOVW	EMB\$W_DV_ERRCNT EQ EMB\$W_DV_UNIT+2 UCB\$W_ERRCNT(R5),(R2)+ ;INSERT NUMBER OF DEVICE ERRORS	
82	70	A5	DO	0069 0069 006D 006D	381 382	ASSUME MOVL	EMB\$L_DV_OPCNT EQ EMB\$W_DV_ERRCNT+2 UCB\$L_OPCNT(R5),(R2)+ ;INSERT OPERATIONS COMPLETED	
50	82 ^{1C}	A5 60	D0	006D 006D 0071	33333333333333333333333333333333333333	ASSUME MOVL MOVL	EMB\$L_DV_OWNUIC EQ EMB\$L_DV_OPCNT+4 UCB\$L_ORB(R5),R0 ;GET ORB ADDRESS ORB\$L_OWNER(R0),(R2)+ ;INSERT VOLUME OWNER UIC	
82	38	A5	DO	006D 0071 0074 0074 0078 0078	388 389	ASSUME MOVL	EMB\$L_DV_CHAR EQ EMB\$L_DV_OWNUIC+4 UCB\$L_DEVCHAR(R5),(R2)+ ;INSERT DEVICE CHARACTERISTICS	
82	0090	C5	98	0078 0078 007D 007D	391 392	ASSUME MOVZBW	EMB\$B_DV_SLAVE EQ EMB\$L_DV_CHAR+4 UCB\$B_SLAVE(R5),(R2)+ ;INSERT SLAVE UNIT NUMBER	
82	20	A1	во	007D 007D	394 395	ASSUME MOVW	EMB\$W_DV_FUNC EQ EMB\$B_DV_SLAVE+2 IRP\$W_FUNC(R1),(R2)+ :INSERT FUNCTION VALUE	
7E	52 02 004F 5E	10 69 50 86 8F 04	C1 30 8ED0 16 BA C0 05	007D 0081 0081 0085 0088 008B 008E 0092	396 397 398 399 400 401 402 30\$: 403 404	ADDL2	EMB\$T_DV_NAME EQ EMB\$W_DV_FUNC+2 #EMB\$C_DV_REGSAV-EMB\$T_DV_NAME,R2,-(SP) ;CALCULATE ADDRESS OF RECEPTION COPY full device name RO ;Copy full device name ;Restore address of register dump area address of register dump area ;CALL REGISTER DUMP ROUTINE ;RESTORE REGISTERS ;REMOVE ENTRY TYPE FROM STACK	GIST
			05	0095 0096	404	RSB .DSABL		

ERRORLOG V04-000 BB D0 3C B6 E1

30 E9 DD B0

BO

70

CO

BO

70

DO

BO

B0

0088

0082

5D 38

52

82

82

38 A5 01A2 57 50

A2 A2 A2 A2

10

82

40 A5

00BC C5

0082 C5

54 A5

56

ERRORLOG

V04-000

EMB\$B_DV_TYPE EQ EMB\$B_DV_CLASS+1
UCB\$B_DEVCLASS(R5),(R2)+; Insert device class and type.

EMB\$B_DV_TYPE+1 EMB\$L_DV_RQPID+4 EMB\$W_DV_BOFF+2

EMB\$L_DV_MEDIA+4

EMB\$W_DV_UNIT+2

EMB\$W_DV_ERRCNT+2

: Clear PID, BOFF and BCNT.

; Insert number of device errors.

EMB\$W_DV_BCNT+2; Insert size of disk.

: Insert unit number.

```
Page
                                                      .SBTTL LOG ASYCHRONOUS DEVICE ATTENTIONS
                          ERL$DEVICEATIN - Log asychronous device attention interrupts that are
                                                    not related to the current I/O operation that may be in progress.
                          INPUTS:
                                                     R5 => UCB
                          OUTPUTS:
$\\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\012345678\\0123456
                                                     If error logging is enabled for the device, an error log buffer is allocated, filled in and released. There may be an error log in progress for the current device, but this is not taken into account since the current attention interrupt is not related to
                                                     the I/O that may be in progress.
                 ERLSDEVICEATTN::
                                                                                     #^M<RO,R1,R2,R3,R6>
UCB$L_DDT(R5),R6
DDT$W_ERRORBUF(R6),R1
UCB$W_ERRCNT(R5)
#DEV$V_ELG,-
UCB$L_DEVCHAR(R5),30$
ERL$ACLOCEMB
R0,30$
R2
                                                     PUSHR
                                                                                                                                                                                                           Save registers.
                                                                                                                                                                                                           Get address of DDT.
R1=size of error log buffer in bytes.
                                                     MOVL
                                                      MOVZWL
                                                      INCW
                                                                                                                                                                                                          Increment number of device errors.
                                                     BBC
                                                                                                                                                                                                            If clr, error log disabled.
                                                     BSBW
                                                                                                                                                                                                           Allocate error message buffer.
If LBC allocation failure.
                                                     BLBC
                                                     PUSHL
                                                                                                                                                                                                          Save address of allocated buffer.
                                                                                       WEMB$C_DA,-
EMB$W_DV_ENTRY(R2)
UCB$W_STS(R5),-
EMB$W_DV_STS(R2)
EMB$Q_DV_IOSB(R2)
                                                     MOVW
                                                                                                                                                                                                           Insert entry type.
                                                                                                                                                                                                   : Save device status in buffer.
                                                     MOVW
                                                     CLRQ
                                                                                                                                                                                                  : Clear irrelevant field.
                                                     ADDL
                                                                                        #EMB$B_DV_CLASS,RZ
                                                                                                                                                                                                  : R2 => device class field.
```

EQ

5

ASSUME

ASSUME ASSUME

ASSUME

ASSUME

ASSUME

ASSUME

ASSUME

CLRQ

MOVL

MOVW

MOVW

EMB\$L_DV_RQPID EMB\$W_DV_BOFF EMB\$W_DV_BCNT

EMB\$L_DV_MEDIA EQ UCB\$L_MEDIA(R5),(R2)+

EMB\$W_DV_UNIT EQ UCB\$W_UNIT(R5),(R2)+

UCB\$W_ERRCNT(R5),(R2)+

EMBSW_DV_ERRCHT EQ

EMB\$L_DV_OPCNT EQ

(R2) +

MOVW

ERRORLOG VO4-000 ERF

```
.SBTTL LOG SOFTWARE STATUS
                            ERL$LOGSTATUS - Log software status corresponding to a logged message.
                                    INPUTS:
                                           RO-R1 contain final I/O status
R2 => MSCP end message
R3 => UCB
R5 => CDRP
                                   OUTPUTS:
                                           An error log message (format EMBSPDEF) is allocated and filled in.
                                           All registers are preserved.
                                 ERL$LOGSTATUS::
                                                     UCBSW_ERRCNT(R3)
#DEV$V_ELG,-
UCB$L_DEVCHAR(R3),20$
 0082 C3
              B6
E1
                                           INCW
                                                                                    : Increment number of device errors.
                                           BBC
                                                                                    : If clear, error log disabled.
71 38 A3
       50
52
8F
                                                     RO,-(SP)
                                           MOVQ
                                                                                    : Save RO, R1, R2.
 7E
              DD 339
                                           PUSHL
                                                     WEMBSK SP LENGTH,R1
ERLSALCOCEMB
R0,10$
 0050
                                           MOVZWL
                                                                                      R1 contains length of buffer to alloc
       12D
   5B
                                           BSBW
                                                                                      Allocate error message buffer.
                                           BLBC
                                                                                    : LBC implies allocation failure.
                                                     #EMB$C_SP,-
EMB$W_SP_ENTRY(R2)
EMB$B_SP_CLASS(R2),R0
 0063 8F
              BO
                                           MOVW
                                                                                    ; Indicate type of error log buffer.
   04 A2
10 A2
              9E
                                           MOVAB
                                                                                    ; RO => where to begin filling.
                                                     UCB$B_DEVTYPE EQ UCB$B_DEVCLASS+1
EMB$B_SP_TYPE EQ EMB$B_SP_CLASS+1
UCB$B_DEVCLASS(R3),(R0)+; Move Device type and class.
                                           ASSUME
                                           ASSUME
   40 A3
              BO
                                           MOVW
                                                     EMB$W SP BOFF EQ
CDRP$Q_BOFF (R5), (R0)+
                                           ASSUME
                                                                                    EMB$B_SP_TYPE+1
              B0
   DO A5
                                           MOVW
                                                                                    ; Copy BOFF.
                                           ASSUME
                                                     EMB$L_SP_BCNT
                                                                                    EMB$W_SP_BOFF+2
   D2 A5
              DO
                                                     CDRPSE_BENT(R5),(R0)+
                                           MOVL
                                                                                    : Also byte count.
                                                     EMB$L SP MEDIA EQ
CDRP$C_MEDIA(R5),(R0)+
                                           ASSUME
                                                                                    EMB$L_SP_BCNT+4
    D8 A5
              DO
                                           MOVL
                                                                                    ; Move media address (LBN).
                                                     EMB$L SP_RQPID EQ
CDRP$C_PID(R5), (R0)+
                                           ASSUME
                                                                                    EMB$L_SP_MEDIA+4
    AC A5
              DO
                                           MOVL
                                                                                    ; Copy requesting PID.
                                                     EMB$Q_SP_IOSB
4(SP),(RO)+
                                           ASSUME
                                                                                    EMB$L_SP_RQPID+4
    04 AE
              70
                                           MOVQ
                                                                                    ; Copy saved I/O status to buffer.
                                                                                    EMB$Q_SP_IOSB+8
                                                     EMBSW SP FUNC EQ CDRPSQ_FUNC(R5),(R0)+
                                           ASSUME
    CO A5
              B0
                                                                                    : Copy 170 function code.
                                           MOVW
                                                     EMB$W_SP_UNIT EQ
UCB$W_UNIT(R3),(R0)+
                                           ASSUME
                                                                                    EMB$W_SP_FUNC+2
              B0
                                           MOVW
                                                                                    ; Copy unit number.
```

H 5

Syn

ERF

ERI

ERRICIONE E ERRICIONE E EN L'ANTIDO DE L'A

ERRORLOG VO4-000

10\$:

54

MOVQ

; Restore Registers RO-R5.

PSE

ERR

Pse

SAE SAE WIC

Pha ---Ini Com Pas Sym Pas Sym Pse Cro

ASS The 130 The 901 41

Mac \$2 230

The

Page 16 (1)

**

MOVC3

POPL

POPR

RSB

20\$:

BA 05

-2(R2)

ERL\$RELEASEMB

EXC

also source address.

Target is -2 since SYSTEMID is 6 bytes.

Restore R2=>Buffer.

#^M<RO,R1,R2,R3,R4,R5> ; Restore registers.

Free Errorlog buffer.

ERRORLOG VO4-000 - ERROR LOG SUPPORT ROUTINES
ERL\$LOG_DMSCP and ERL\$LOG_TMSCP

022F 701 .dsabl lsb

16-SEP-1984 00:04:39 VAX/VMS Macro V04-00 5-SEP-1984 03:41:34 [SYS.SRC]ERRORLOG.MAR;1

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EXI

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.SBTTL BUILD STARTUP AND POWERFAIL MESSAGES ERL\$COLDSTART - LOG COLDSTART (SYSTEMBOOT) THIS ROUTINE IS CALLED BY SYSINIT AFTER CORRECTLY SETTING THE SYSTEM TIME TO LOG THE BOOTING OF THE SYSTEM. ERL\$WARMSTART - LOG WARMSTART (POWER RECOVERY) THIS ROUTINE IS CALLED BY POWERFAIL AFTER CORRECTING THE SYSTEM TIME TO LOG THE POWER FAIL AND RECOVERY. INPUTS: NONE OUTPUTS: AN ERROR LOG BUFFER IS ALLOCATED AND FILLED WITH THE APPROPRIATE MESSAGE ; IF POSSIBLE AND THE ERROR LOG PROCESS AWAKENED IF NECESSARY. ERL\$COLDSTART:: MOVZWL #EMB\$C_CS,R3 BRB ERL\$WARMSTART:: MOVZWL

720 721 7223 7225 7226 7228 7230 7333 7336 7337 53 20 3C #EMB\$C_WS,R3
#EMB\$C_SU_LENGTH,R1
ERL\$ALCOCEMB
R0,20\$
EXE\$READ_TODR
R0,EMB\$L_SU_DAYTIM(R2)
R3,EMB\$W_SU_ENTRY(R2)
ERL\$RELEASEMB 53 300960005 1000005 MOVZWL **BSBB** BLBC 0'EF 50 53 0005 00000000 JSB 10 A2 04 A2 MOVL MOVW BSBW 20\$: RSB .DSABL LSB

SET TYPE OF MESSAGE TO COLDSTART

SET TYPE OF MESSAGE TO WARMSTART SET SIZE OF MESSAGE TO ALLOCATE ALLOCATE AN ERROR LOG BUFFER BRANCH IF NONE AVAILABLE GET TIME TO LOG LOG TIME OF DAY CLOCK SET MESSAGE TYPE RELEASE BUFFER

.SBTTL ALLOCATE ERROR MESSAGE BUFFER ERLSALLOCEMB - ALLOCATE ERROR MESSAGE BUFFER

THIS ROUTINE IS CALLED TO ALLOCATE AN ERROR LOG MESSAGE BUFFER AND INITIALIZE ITS HEADER.

INPUTS:

R1 = SIZE OF MESSAGE BUFFER REQUIRED IN BYTES.

OUTPUTS:

RO LOW BIT CLEAR INDICATES AN ALLOCATION FAILURE.

RO LOW BIT SET INDICATES SUCCESSFUL ALLOCATION WITH:

R1 = ERROR SEQUENCE NUMBER. R2 = ADDRESS OF ALLOCATED ERROR MESSAGE BUFFER.

IN EITHER CASE THE UNIVERSAL ERROR SEQUENCE NUMBER IS INCREMENTED AND STORED IN THE BUFFER AT THE STANDARD PLACE, ALONG WITH THE TIME. AND THE ERROR LOG PROCESS MAY BE AWAKENED IF AN ERROR ALLOCATION BUFFER IS FOUND TO BE FULL.

R3 IS PRESERVED ACROSS CALL.

```
ERL$ALLOCEMB::
                                                                                                                                                                                                                                                        ; ALLOCATE ERROR MESSAGE BUFFER
                                                                                                                                                                    #EMB$K_LENGTH,R1 ; Add in size of header for message
ERL$GB_BUFIND,RO ; GET CURRENT ALLOCATION BUFFER INDICATOR
ERL$AL_BUFADDR[RO],RO ; GET ADDRESS OF ALLOCATION BUFFER DESCRIPTOR
#ERL$V_LOCK,ERL$B_FLAGS(RO),15$; IF SET, BUFFER BEING COPIED
ERL$L_NEXT(RO),R2 ; GET ADDRESS OF NEXT AVAILABLE SPACE
R1,R2,ERL$L_NEXT(RO) ; CALCULATE ADDRESS OF NEXT AVAILABLE SPACE
ERL$L_END(RŪ),ERL$L_NEXT(RO) ; ENTRY FIT WITHIN BUFFER?
20$
#ERL$M_TIMER.ERL$GB_BUFFLAG ; SET TIMER ACTIVE
#1,ERL$GB_BUFFIM ; FORCE ERROR LOG PROCESS WAKE
ERL$L_END(RO),ERL$L_NEXT(RO) ; INDICATE THAT BUFFER IS FULL
#1,ERC$GB_BUFIND ; SWITCH TO ALTERNATE BUFFER
ERL$GB_BUFIND,RO ; GET ADDRESS OF ALLOCATION BUFFER DESCRIPTOR
#ERL$AL_BUFADDR[RO],RO ; GET ADDRESS OF ALLOCATION BUFFER DESCRIPTOR
#ERL$V_LOCK,ERL$B_FLAGS(RO),17$; IF SET, BUFFER BEING COPIED
R1,ERL$L_NEXT(RO),R2 ; CALCULATE ADDRESS OF NEXT AVAILABLE SPACE
ERL$L_END(RO),R2 ; ENTRY FIT WITHIN BUFFER?
10$
ERL$L_END(RO),ERL$L_NEXT(RO) ; INDICATE THAT BUFFER IS FULL

**IF GEQU YES*
**ERL$L_END(RO),ERL$L_NEXT(RO) ; INDICATE THAT BUFFER IS FULL
                                                                                                                                                                                                                                                        ; DISABLE ALL INTERRUPTS
                                                                                                                                             DSBINT
                                                                                                                                             MOVZBL
                                                               09A
00
00
00
01
01
                00000408'EF
         00000400 EF40
                                                                                                                                             MOVL
         1E 03 A0
                                                                                                                                            BBS
                                   04
                                                                                                                 105:
                                                                                                                                             MOVL
  04 A0 52 51
04 A0 08 A0
                                                                                                                                             ADDL3
                                                                                                                                             CMPL
                                                               18900CA900C11E041CD3996
                                                                                                                                             BGEQU
00000409'EF
                                                                                                                                             BISB
0000040B'EF
                                                                                                                                             MOVB
04 A0
00000408'EF
                                   08 AO
                                                                                                                 15$:
                                                                                                                                             MOVL
                                              01
                                                                                                                                             XORB
               00000408'EF
                                                                                                                                             MOVZBL
         00000400'EF40
                                                                                                                                             MOVL
         OB 03 A0
04 A0
52 0
                                                                                                                                             BBS
                                                                                                                                             ADDL3
                                    08
                                                                                                                                             CMPL
                                                                                                                                                                       10$

ERL$L_END(RO), ERL$L_NEXT(RO); INDICATE THAT BUFFER IS FULL
RO; INDICATE ALLOCATION FAILURE
                                                                                                                                             BGEQU
          04 AO
                                    08
                                                                                                                  175:
                                                                                                                                             MOVL
                                                                                                                                             CLRL
                                                                                                                                             BRB
                                                                                                                                                                      #EMB$K_LENGTH.R2 ; Point past the message header
#PR$ SID.EMB$L_HD_SID(R2) ; Set system ID into message
R1.EMB$W_SIZE(R2) ; Set size in message header
ERL$B_BUFIND(R0).EMB$B_BUFIND(R2) ; SET RESPECTIVE BUFFER INDICATOR
ERL$B_BUSY(R0) ; INCREMENT MESSAGE BUSY COUNT
ERL$GC_SEQUENCE.R1 ; GET CURRENT ERROR SEQUENCE NUMBER
                                                                                                                  20$:
                                                                                                                                             ADDL
                                                                                                                                             MFPR
         FE AZ
                                                                                                                                             MOVZWL
                                    02
                                               AO
                                                                                                                                             MOVB
                                                                                                                                             INCB
                                                                DO
                 00000410
                                                                                                                                             MOVL
```

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ERRORLOG V04-000			- ER	ROR LO	S SUPI	PORT ROMESSAGE	UTINES BUFFER	16-SEP-1984 00: 5-SEP-1984 03:	:04:39	VAX/VMS Macro V04-00 [SYS.SRC]ERRORLOG.MAR;1	Pag	je 21 (1
	06 A2	00000000 EF 0E A2 51 50 01 00000410 EF	70 80 00 06	02D8 02E0 02E4 02E7 02ED 02F0	796 797 798 799 800 801	30\$:	MOVQ MOVW MOVL INCL ENBINT RSB	EXESGQ_SYSTIME, EMBSQ_DV_R1,EMBSW_DV_ERRSEQ(R2) #1,R0 ERLSGL_SEQUENCE	: INCREM	; INSERT CURRENT TIME ERROR SEQUENCE NUMBER ICCESS INDICATOR MENT UNIVERSAL ERROR SEQUE INTERRUPTS	NCE NU	IMBER

EXI

```
0 6
                                 16-SEP-1984 00:04:39 VAX/VMS Macro V04-00 5-SEP-1984 03:41:34 [SYS.SRC]ERRORLOG.MAR;1
          .SBTTL GET FULL DEVICE NAME
FRLSGETFULLNAME - GET FULL DEVICE NAME
  THIS ROUTINE IS CALLED TO COPY THE FULL DEVICE NAME (NODE NAME + DEVICE NAME) TO THE ERROR LOG BUFFER.
```

INPUTS: R3 = address of DDB R2 = address of error log buffer

OUTPUTS:

If a node name exist in the system block, it is copied with the device name to the error log buffer.

RO, R1, AND R3 ARE DESTROYED ACROSS CALL.

```
ERL$GETFULLNAME::
                                                                   DDB$T_NAME(R3),R1
(R1)+,-(SP)
                                                        MOVAB
                                                                                                          Get address of device name.
                      9EA03189950190955
                                                                                                          Save the string length
Get address of system block
                                                        MOVZBL
                                                                   DDB$L_SB(R3),R3
20$
SB$T_NODENAME(R3),R3
(R3)*,R0
                                                        MOVL
                                                                                                          If EQL, go to move device name
Get address of nodename
                                                        BEQL
                                                        MOVAB
       50
                                                                                                           Get nodename length
                                                        MOVZBL
                                                                                                          If eql, go move device name
Nodename length + device name
Total string len. + 1 for '$'
                                                        BEQL
ADD83
                                                                    20$
                                                                   RO, (SP), (R2)
(R2)+
(R3)+ (R2)+
RO, 10$
#^A/$/, (R2)+
30$
62
       6E
              50283043EE10
                                                        INCB
       82
                                            10$:
                                                        MOVB
                                                                                                          Copy nodename
         FA
                                                        SOBGIR
       82
                                                        MOVB
                                                                                                       : Insert the "$"
                                                        BRB
                                                                                                          Go move device name
    82
50
82
FA
                                                                    (SP),(R2)+
                                            20$:
30$:
40$:
                                                        MOVB
                                                                                                        ; Move dev. name len. to buffer
                                                        MOVL
                                                                    (SP)+,R0
                                                                                                        ; Get dev. name length
                                                                   (R1)+,(R2)+
R0,40$
                                                                                                       ; Move device name into buffer
                                                        MOVB
                                                        SOBGTR
                                                        RSB
                                                                                                       ; Return to caller
```

EXI

Page

16-SEP-1984 00:04:39 5-SEP-1984 03:41:34 VAX/VMS Macro V04-00 [SYS.SRC]ERRORLOG.MAR; 1

.SBTTL RELEASE ERROR MESSAGE BUFFER ERL\$RELEASEMB - RELEASE ERROR MESSAGE BUFFER

THIS ROUTINE IS CALLED TO RELEASE AN ERROR MESSAGE BUFFER FOR PROCESSING BY THE ERROR LOG PROCESS.

INPUTS:

R2 = ADDRESS OF ERROR MESSAGE BUFFER.

OUTPUTS:

THE COMPLETED ERROR MESSAGE COUNT IS INCREMENTED IN THE RESPECTIVE ALLOCATION BUFFER H ADER, THE MESSAGE IS SET VALID, AND THE BUSY MESSAGE COUNT IS DECREMENTED IN THE RESPECTIVE ALLOCATION BUFFER HEADER.

R3 IS PRESERVED ACROSS CALL.

859 860 861 862 863 864 865 866 867 868 869 870 871 872 873 874 96 A 0 8 3 1 A 0 5 0 5 00000400 EF40 60 OOFF 8F 8F 01 0A 07 OD 00000409'EF 01 A0 01 0000040B'EF

ERRORLOG V04-000

ERL\$RELEASEMB:: INCB MOVZBL MOVL ADAWI BBCS CMPB BGTRU #1, ERL\$GB_BUFTIM MOVB RSB

#AXFF, ERL\$GB_BUFFLAG, 10\$; IF CLR, NO TIMER RUNNING

#MAXMSG, ERL\$B_MSG(NT(RO); MAXIMUM NUMBER OF MESSAGES EXCEEDED?

IRELEASE ERROR MESSAGE BUFFER

SET MESSAGE BUFFER VALID

SET MESSAGE BUFFER VALID

SET MESSAGE BUFFER VALID

SET MESSAGE BUFFER

SET MESSAGE BUFFER FORCE ERROR LOG PROCESS WAKE

10\$:

RSB

.END

EX

ERRORLOG Symbol table	- ERROR LOG SUP		6-SEP-1984 00:04:39 VAX/VMS Macro V04-00 5-SEP-1984 03:41:34 [SYS.SRC]ERRORLOG.MAR;1	Page	25
ADP_HANDLER ADP_UNEXP BUF1 BUF2 BUG\$ UNXINTEXC CDDB\$B_SYSTEMID CDDB\$Q_CNTRLID CDRP\$L_BCNT CDRP\$L_BCNT CDRP\$L_PID CDRP\$L_PID CDRP\$L_PID CDRP\$L_PID CDRP\$L_PID CDRP\$L_SB DDB\$T_NAME DDT\$L_REGDUMP DT\$L_REGDUMP DDT\$L_REGDUMP EMB\$L_DV_LENGTH EMB\$C_UI EMB\$C_U	00000000 R 00000000 R 00000000 R 00000000	EMB\$L_UI_CSR EMB\$L_UI_TR EMB\$Q_DV_IOSB EMB\$Q_DV_TIME EMB\$Q_DV_NAME EMB\$T_DEVNAM EMB\$T_DV_BCNT EMB\$W_DV_BCNT EMB\$W_DV_ERRCNT EMB\$W_SP_ENTRY EMB\$W_SP_ENTRY EMB\$W_SP_ENTRY EMB\$W_SP_ENTRY EMB\$W_SP_UNIT EMB\$W_SP_ENTRY EMB\$W_SP_ENTRY ERB\$W_SP_UNIT EMB\$W_SP_ENTRY ERB\$W_SP_ENTRY ERB\$W_SP	5-SEP-1984 05:41:54		(1

EX

ERRORLOG Symbol table	- ERROR LOG SUPPORT R	DUTINES H 6	16-SEP-1984 00:04:39 VAX/VMS Macro V04-00 5-SEP-1984 03:41:34 ESYS.SRCJERRORLOG.MAR;1	Page	26
ERL\$VEC100 ERL\$VEC108 ERL\$VEC112 ERL\$VEC112 ERL\$VEC112 ERL\$VEC120 ERL\$VEC124 ERL\$VEC128 ERL\$VEC136 ERL\$VEC136 ERL\$VEC136 ERL\$VEC136 ERL\$VEC160 ERL\$VEC160 ERL\$VEC166 ERL\$VEC168 ERL\$VEC168 ERL\$VEC168 ERL\$VEC192 ERL\$VEC192 ERL\$VEC200 ERL\$VEC200 ERL\$VEC200 ERL\$VEC200 ERL\$VEC200 ERL\$VEC200 ERL\$VEC200 ERL\$VEC200 ERL\$VEC206	00000088 RG 03	0826 0826 08112 04826 04826 04826 05332228 0533222333444826 05333333444826 06823333334444826 06823333334444826 06823333334444826 06823333334444826 06823333334444826 06823333334444826 06823333334444826 06824444228 06824444826 068244448826 068244448826 06824448826 068244448826 06824448826 068244448826 06824448826 068244448826 068244448826 068244448826 06824448826 068244448826 068244448826 068244448826 068244448826 068244448826 068244448826 06824448826 068244448826 068244448826 068244448826 068244448826 068244448826 068244448826 06824448826 06824448826 068244448826 068244448826 068244448826 06824448826 06824448826 06824448826 06824448826 06824448826 06824448826 06824448826 06824448826 06824448826 06824448826 0682	00000034 RG 03 00000088 RG 03 00000000 RG 03 00000000 RG 03 00000001 RG 03 0000001 RG 03 0000002 RG 03 0000003 RG 03 0000003 RG 03 000000 RG 03 00000 RG 03 00000 RG 03 00000 RG 03 00000 RG 03 000000 RG 03 00000 RG 03 0000 RG 03		

EXI VO

EXI

EXC VO4

16-SEP-1984 00:04:39 VAX/VMS Macro V04-00 5-SEP-1984 03:41:34 [SYS.SRC]ERRORLOG.MAR;1

Psect synopsis!

PSECT name	Allocation	PSECT No.	Attributes				
*ABS . \$ABS\$ \$\$\$260 \$AEXENONPAGED WIONONPAGED	00000000 (0.) 00000000 (0.) 00000414 (1044.) 00000007 (199.) 00000370 (880.)	00 (0.) 01 (1.) 02 (2.) 03 (3.) 04 (4.)	NOPIC USR NOPIC USR NOPIC USR NOPIC USR NOPIC USR	CON ABS	LCL NOSHR	NOEXE NORD EXE RD EXE RD EXE RD EXE RD	NOWRT NOVEC BYTE WRT NOVEC BYTE WRT NOVEC QUAD WRT NOVEC LONG WRT NOVEC BYTE

! Performance indicators !

Phase	Page faults	CPU Time	Elapsed Time
Initialization	36	00:00:00.07	00:00:01.74
Command processing	36 108 549	00:00:00.49	00:00:04.75
Symbol table sort	0	00:00:03.40	00:00:11.63
Pass 2	174 34	00:00:04.51	00:00:14.71
Symbol table output Psect synopsis output	34	00:00:00.28	00:00:01.92
Cross-reference output	ō	00:00:00.00	00:00:00.00
Assembler run totals	905	00:00:32.44	00:01:44.15

The working set limit was 1950 pages.
130939 bytes (256 pages) of virtual memory were used to buffer the intermediate code.
There were 120 pages of symbol table space allocated to hold 2271 non-local and 34 local symbols.
901 source lines were read in Pass 1, producing 25 object records in Pass 2.
41 pages of virtual memory were used to define 40 macros.

+-----! Macro library statistics !

Macro Library name

_\$255\$DUA28:[SYS.OBJ]LIB.MLB;1 _\$255\$DUA28:[SYSLIB]STARLET.MLB;2 TOTALS (all libraries)

Macros defined

28

35

2304 GETS were required to define 35 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:ERRORLOG/OBJ=OBJ\$:ERRORLOG MSRC\$:ERRORLOG/UPDATE=(ENH\$:ERRORLOG)+EXECML\$/LIB

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